

**Amendments to the Claims:**

Please cancel claims 5-10 and amend claims 11-13, 16 and 17, as shown in the following listing of claims.

This listing of claims will replace all prior versions and listings of claims in the application.

1-10. (cancelled)

11. (currently amended) The An apparatus as in claim 5, wherein for providing an adjustable bandwidth high pass filter, the apparatus further comprises comprising:

a high pass filter having an input capacitor coupled in series with a resistive ladder having a plurality of resistors coupled in series, the coupling between the capacitor and a first resistor of the resistive ladder defining a first tap and successive couplings between resistors forming successive taps, a last resistor of the resistive ladder being coupled to a ground;

a plurality of bandwidth adjusting resistors, each having a first side coupled to the first tap; and

a plurality of MOSFET devices, each MOSFET device having a source, a gate and a drain, the source of each MOSFET device coupled to the a second side of one of said a corresponding bandwidth adjusting resistor[[s]];

the drain of the each MOSFET device coupled to said ground and said the gate being coupled to a corresponding control circuit.

12. (currently amended) The apparatus as in claim 11, wherein the each control circuit comprises:

an amplifier output coupled to the gate of the corresponding switching MOSFET device.

13. (currently amended) The apparatus as in claim 12, wherein the each amplifier further comprises:

a tristate buffer amplifier; and

a pull-up resistance coupled between the output of the tristate buffer amplifier and a power supply.

14. (previously presented) The apparatus as in claim 13, wherein the power supply is the power supply for an integrated circuit containing the apparatus.

15. (previously presented) The apparatus as in claim 13, wherein the pull-up resistance comprises a long channel triode device.

16. (currently amended) The apparatus as in claim 11, further comprising a capacity plurality of capacitors, each disposed between the gate and source of the a corresponding MOSFET switch device.

17. (currently amended) The apparatus as in claim 15, wherein the drain of the long channel triode device is coupled to a power supply voltage, the gate to the ground of the power supply voltage and the source is coupled to the gate of the corresponding MOSFET switch device.